

Product Summary

Micro2.3 is segment LED display for the launch of a new low-cost miniaturized innovative LED systems designed by YDEA-TECH, mainly for the light of the screen, mesh screen display, spot light, shaped screen.

Micro2.3 size only (73 mm x 15 mm), the design can save space and reduce external cable Screen, Screen to simplify design and reduce design complexity, while the highly price competitive force. With this system, you can help customers achieve unprecedented innovative design. It solved the Screen space is limited, Screen protection problems, service problems, and the price puzzle, will further differentiate products designed to provide a competitive advantage.

Product Feature

- Operating voltage: 3.6-5V DC.
- Largest single block scanning plate with a load of 4096 pixels.
- Signal block of scan board RGB output serial data clock 24 groups, 4 clk extensions.
- Ultra-small size design (73mm x 15 mm), designed to solve the space problem.
- Support within the 4096 -point, intelligent tracing point set.
- Supports chromaticity correction within 2048 points, 4096 points within a brightness correction.
- Configuration parameters to read and, receiving card state detection.
- Support single-card position any offset single card display rotation to achieve shaped screen.
- Reduce the number of cables and connectors, simplifying design LED display. Signal transmission requires only two core UTP twisted pair, allowing the display signal and power wiring into one design, peripherals cascade connection line from the traditional binary two into one into one.
- Display light board can be integrated with the scanning plate modular design, faulty only when the module is individually removable replacement, let Repairs easier, reduce maintenance costs later.
- Fully enclosed design, effectively shielding, allowing the display to easily pass EMI testing, reduce waterproof design challenges.

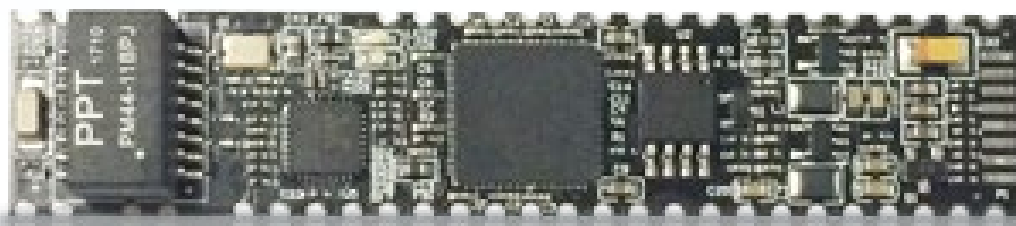
Technical Specifications

The maximum load capacity	4096 pixels
Refresh rate	Static screen up to 5500Hz over



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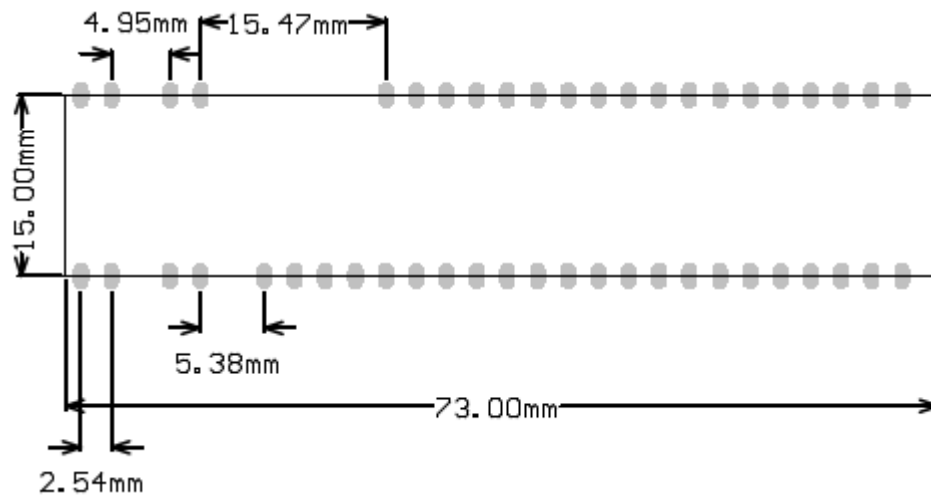
Scan board Specification

Micro-2.3

Version: V01 Date: 2018-03-10

Interface Type	2.54mm, Stamp hole interface
Scanning mode	Static - 32 scanning
Gray levels	4096—65536
Chip supports	Conventional chip, PWM chips, lighting chip
Number of outputs RGB data set	8 parallel RGB data, 24 groups serial data
Shaped show	Any offset single card position
Single card rotation	0° ,90° ,180° ,270°
Online Upgrade	Support
Cascading number of cards	In general value of 128, the maximum value of 256
Loss of brightness	--
Operating voltage	3.6-5V DC
Operating temperature	-40℃-75℃
Dimensions	Length73 * width15 (mm)
Random mapping	Support

Board Card Size



Interface Definition

Under all the many different working modes of it, different working modes can output different data. Interfaces are defined as follows:

- 1) 24 RGB data serial mode, maximum support for 4 sets of clock extensions, scan drive (direct decoding, 138, 5958 decoder mode), defined as follows:

(support for 138 and 595, the maximum support for eight scans, the maximum support for four sweeps)

TX_P	1	2	RX_N
TX_N	3	4	RX_P
	5	6	
EARTH	7	8	EARTH
EARTH	9	10	EARTH
	11	12	
3V3	13	14	
GND	15	16	
LED	17	18	
SW	19	20	
B	21	22	DATA24
A	23	24	DATA23
OE	25	26	DATA22
LE	27	28	DATA21
CLK4(D)	29	30	DATA20
CLK3(C)	31	32	DATA19
CLK2	33	34	DATA18
CLK1	35	36	DATA17
DATA8	37	38	DATA16
DATA7	39	40	DATA15
DATA6	41	42	DATA14
DATA5	43	44	DATA13
DATA4	45	46	DATA12
DATA3	47	48	DATA11
DATA2	49	50	DATA10
DATA1	51	52	DATA9
5V_IN	53	54	GND
5V_IN	55	56	GND

138 decoding signal A (the second definition of DCLK ,when it is translated from 5958);
 138 decoding signal B (the second definition of DIN ,when it is translated from 5958);
 138decoded signal C (the second definition of BK,when it is translated from 5958);

- 2) Parallel 8 groups, maximum support for 4 sets of clock extensions, scan drive (direct decoding, 138, 5958 decoder mode), defined as follows:

(support for 138 and 595, the maximum support for eight scans, the maximum support for four sweeps)

TX_P	1	2	RX_N
TX_N	3	4	RX_P
	5	6	
EARTH	7	8	EARTH
EARTH	9	10	EARTH
	11	12	
3V3	13	14	
GND	15	16	
LED	17	18	
SW	19	20	
B	21	22	B8
A	23	24	G8
OE	25	26	R8
LE	27	28	B7
CLK4(D)	29	30	G7
CLK3(C)	31	32	R7
CLK2	33	34	B6
CLK1	35	36	G6
G3	37	38	R6
R3	39	40	B5
B2	41	42	G5
G2	43	44	R5
R2	45	46	B4
B1	47	48	G4
G1	49	50	R4
R1	51	52	B3
5V_IN	53	54	GND
5V_IN	55	56	GND

138 decoding signal A (the second definition of DCLK ,when it is translated from 5958);
138 decoding signal B (the second definition of DIN ,when it is translated from 5958);
139decoded signal C (the second definition of BK,when it is translated from 5958);

- 3) When the actual extension after using the four groups of the clock signal, scanning signal can only meet A, B signal, if you use A, B, C, D, scanning signal, the clock can only extend two groups;

